CERTIFICATE OF MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents and Trademarks, Washington, D.C. 20231, on November 14, 2001.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: November 14, 2001

RECEIVED

Kishore KARIGHATTAM et al.

JAN 2 3 2002

Serial No.: 09/905,132

Group Art Unit: 2643

Technology Center 2600

Filed: July 13, 2001

Examiner: To Be Assigned

For:

MECHANISM TO STRIP LARQ HEADER AND PRESERVE LARQ HEADER IN

STATUS FRAME

Assistant Commissioner for Patents

Washington, D.C. 20231

TRANSMITTAL LETTER

Sir:

Please find enclosed the following:

- 1) Transmittal Letter (original and a copy)
- Postcard

3) The fee for claims has been calculated as shown below:

X Large En	tity Small Entity				
Fee Items	Claims remaining after Amendment	Highest # of Claims previously paid for	Extra Claims	Fee Rate	TOTAL
Total Claims	12	- 20=	0	x 18	\$ 0.00
Independent	3	- 3=	0	x 84	\$ 0.00
Multiple Dependent Claim fee (one or more, first added by this amendment) 280.00					\$ 0.00
Total Fees					\$ 0.00

If any unresolved issues remain, please contact Applicant's attorney at the telephone number below. The Commissioner is hereby authorized to charge any fees associated with this communication, or credit any overpayment, to Deposit Account No. 01-0365 (AMD, Inc.).

Respectfully submitted,

November 14, 2001

Date

Michele Liu

Attorney for Applicants

Reg. No. 44,875

(650) 493-4540



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Michele Liu

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IN STATUS FRAME

Assistant Commissioner of Patents Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir: Please amend the application as follows:

IN THE CLAIMS

Please add the following claims:

9. (New) A system, comprising:

an Ethernet controller; and

a home phone line network controller, wherein the home phone line network controller comprises:

a first logic block for detecting a LARQ header in a frame with a priority tag,

a second logic block for stripping the LARQ header and a FCS in the frame with the priority tag, and

a third logic block for recalculating the FCS for the stripped frame with the